

# V2V EDTECH LLP

Online Coaching at an Affordable Price.

# **OUR SERVICES:**

- Diploma in All Branches, All Subjects
- Degree in All Branches, All Subjects
- BSCIT/CS
- Professional Courses
- +91 93260 50669
  v2vedtech.com
- V2V EdTech LLPv2vedtech





(ISO/IEC - 27001 - 2005 Certified)

#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System

**Subject Code:** 

22516

#### **Important Instructions to examiners:**

- 1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills.
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for anyequivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.
- 8) As per the policy decision of Maharashtra State Government, teaching in English/Marathi and Bilingual (English + Marathi) medium is introduced at first year of AICTE diploma Programme from academic year 2021-2022. Hence if the students in first year (first and second semesters) write answers in Marathi or bilingual language (English +Marathi), the Examiner shall consider the same and assess the answer based on matching of concepts with model answer.

Q.	Sub		Answer		Marking
No	Q.N.				Scheme
1.	a)		10	ed and Multitasking operating	10 2M
	Ans.	Features	Multiprogramming	Multitasking	Any two
		Basic	It allows multiple programs to utilize the CPU simultaneously.	A supplementary of the multiprogramming system also allows for user interaction.	relevant points, 1M each
		Mechanis m	Based on the context switching mechanism.	Based on the time- sharing mechanism.	
		Objective	It is useful for reducing/decreasing	It is useful for running multiple processes at the	



#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System

Subject Code:

	Execution Execution CPU Switching Timing	CPU idle time and increasing throughput as much as possible. When one job or process completes its execution or switches to an I/O task in a multi-programmed system, the system momentarily suspends that process. It selects another process from the process scheduling pool (waiting queue) to run. In a multiuser environment, the CPU switches between programs/processes quickly. It takes maximum time to execute the	increasing CPU and system throughput. In a multiprocessing system, multiple processes can operate simultaneously by allocating the CPU for a fixed amount of time. In a single-user environment, the CPU switches between the processes of various programs. It takes minimum time to	
		time to execute the process.	execute the process.	
b) L Ans.	<ul> <li>User Int</li> <li>Program</li> <li>I/O Ope</li> <li>File syst</li> <li>Commu</li> <li>Error De</li> <li>Resourc</li> <li>Account</li> </ul>	a Execution ration tem Manipulation nication etection e Allocation	5.	2M <sup>1/2</sup> M each for any 4 services



Г

٦

(ISO/IEC - 27001 - 2005 Certified)

Subject: Oper	rating System		Subject Code:	225	16
c) Ans.	<b>Define : Process</b> <b>Process:-</b> A proce as job, task or un	ess is a program in exect	ution. Process is also ca		2M Correct Definition
	information of th	Control Block is a dat the process related to it. T ask control block, entry o	he process control bloc	ains	IM each
d) Ans.		<b>I/O burst cycle.</b> <b>:</b> It is a time period when It is a time period when			2M Correct Definition
e)	with I/O resource	-			1M each 2M
Ans.	Parameters	Paging	Segmentation		Any two relevant
	Individual Memory	In Paging, we break a process address space into blocks known as pages.		of ak ice	differences – 1M each
	Memory Size	The pages are blocks of fixed size.	The sections/segment are blocks of varyit sizes.		
	Accountability	The OS divides the available memory into individual pages.	The compiler main calculates the size individual segmen their actual address well as virtual address	of its, as	
	Speed	This technique is comparatively much faster in accessing memory.	This technique comparatively mu slower in accessi memory than Paging.		
	Size	The available memory determines the individual page sizes.	The user determines t individual segme sizes.	ent	
	Fragmentation	The Paging technique may underutilize some of the pages- thus	The Segmentati technique may not u some of the memo	ise	

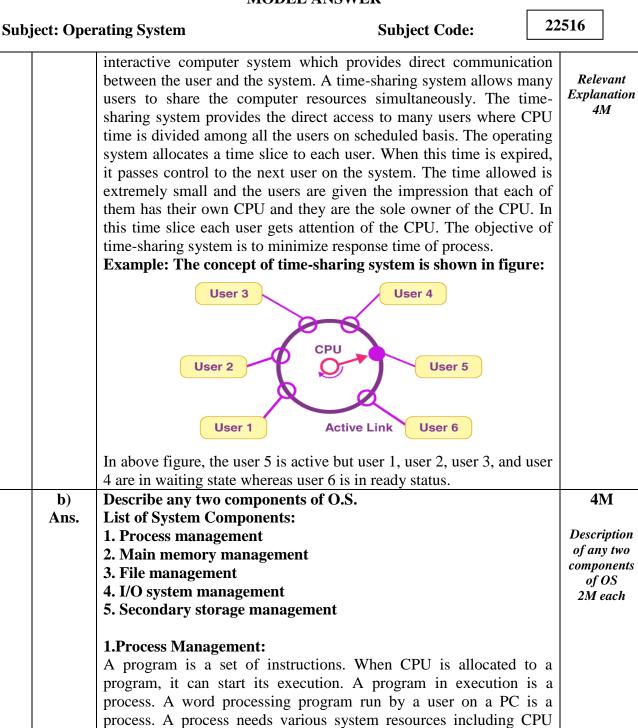


Г

Subj	ject: Oper	rating System		Subject Code:	22516
			leading to internal fragmentation.	blocks at all. Thus, may lead to extern fragmentation.	
		Logical Address	A logical address divides into page offset and page number in the case of Paging.	A logical addre divides into secti offset and secti number in the case Segmentation.	on on
		Data Storage	In the case of Paging, the page table leads to the storage of the page data.	In the case	
	f) Ans.	Write syntax of (i) Kill (ii) Sleep i) kill Syntax: kill Pid	following commands-		2M 1M for each correct syntax
		ii) sleep	JMBER[SUFFIX]		
	g) Ans.	<ul><li>Deleting a</li><li>Appendin</li><li>Renaming</li></ul>	a file file: a file: ning within a file a file ng new information to the g an existing file. copy of a file, copy file to		2M Any four operations <sup>1</sup> / <sub>2</sub> M each
2.	a) Ans.	<b>Explain Time sh</b> In time sharing s among them. Th	<b>IREE of the following:</b> <b>haring O.S.</b> ystem, the CPU executes he switches occur so fre- ach program while it i	equently that the users	can



#### WINTER – 2022 EXAMINATION MODEL ANSWER



time, memory, files and I/O devices to complete the job execution. These resources can be given to the process when it is created or

allocated to it while it is running.



Г

(ISO/IEC - 27001 - 2005 Certified)

Subject: Operating System	Subject Code:	22516
The operating system responsib	ble for the following activitie	s in
connection with process managen	nent:	
Creation and deletion of user	and system processes.	
Suspension and resumption of	• •	
A mechanism for process syn	chronization.	
A mechanism for process com		
• A mechanism for deadlock ha		
2. Main-Memory Management	6	
Main memory is a large array of	words or bytes, ranging in size f	from
hundreds of thousands to billion		
address. Main memory is a rep	pository of quickly accessible	data
shared by the CPU and I/O de	vices. The central processor re-	eads
instructions from main memory d	luring the instruction fetch cycle	and
both reads and writes data from	• •	
cycle. The main memory is gene		vice
that the CPU is able to address an	•	
The operating system responsib		s in
connection with main memory s r	-	
	of memory are currently being u	used
and by whom.		
	parts thereof) and data to move	
	ting and deallocating memory sp	pace
as needed.		
3. File Management		
A file is a collected of related	•	
Computer can store files on the		
provide long term storage. Son magnetic tape, magnetic disk and		
has its own properties like speed,	1	
access methods. A file system no		
ease their use. These director		
directions.	tes may contain mes and c	
The operating system responsib	ble for the following <b>activitie</b>	s in
connection with file management	-	
The creation and deletion of f		
<ul> <li>The creation and deletion of d</li> </ul>		
	manipulating files and directions	S
<ul> <li>The support of primitives for a</li> <li>The mapping of files onto sec</li> </ul>		
<ul> <li>The backup of files on stable</li> </ul>		



(ISO/IEC - 27001 - 2005 Certified)

#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System

Subject Code:

c) Ans.	Explain shared memory model of Interprocess communication (IPC) Inter-process communication: Cooperating processes require an Inter- process communication (IPC) mechanism that will allow them to exchange data and information.	4M Explanation 3M Diagram 1M
	<ul> <li>4. I/O device Management Input / Output device management provides an environment for the better interaction between system and the I / O devices (such as printers, scanners, tape drives etc.). To interact with I/O devices in an effective manner, the operating system uses some special programs known as device driver. The device drivers take the data that operating system has defined as a file and then translate them into streams of bits or a series of laser pulses (in regard with laser printer). The I/O subsystem consists of several components: <ul> <li>A memory management component that includes buffering, caching, spooling</li> <li>A general device driver interface</li> <li>Drivers for specific hardware devices</li> </ul> </li> <li>5. Secondary-Storage Management The computer system provides secondary storage to back up main memory. Secondary storage is required because main memory is too small to accommodate all data and programs, and the data that it holds is lost when power is lost. Most of the programs including compilers, assemblers, word processors, editors, and formatters are stored on a disk until loaded into memory. Secondary storage consists of tapes drives, disk drives, and other media. The operating system is responsible for the following activities in connection with disk management:     Free space management     Storage allocation     Disk scheduling.</li> </ul>	



#### WINTER – 2022 EXAMINATION MODEL ANSWER

Subject: Open	rating System	Subject Code:	22516
	Shared memory       Process P1         Shared Region of P1       Process P2         Process P2       Kernel         Shared Memory Sy         In this, all processes who want processes can access a region of address space of a process creating a         All the processes using the shared memory segment.         The form of data and location are of who want to communicate with each         These processes are also responsible writing to the same location simulta         After establishing shared memory segment are treated	to communicate with or the memory residing in a shared memory segment. memory segment should at nemory. All the processes and/or writing data in sh determined by these proce n other. ontrol of the operating syst for ensuring that they are neously. segment, all accesses to	n an tach can ared esses eem. e not the
d) Ans.	<ul> <li>without assistance of kernel.</li> <li>Describe different scheduling criteria</li> <li>CPU utilization: - In multiprogram keep CPU as busy as possible. CPU to 100 percent.</li> <li>Throughput: - It is the number of per unit time. It is a measure of we CPU is busy in executing processe the system. Throughput depends on for any process.</li> </ul>	The main objective of J utilization can range from processes that are complored done in the system. We so that work is being done done done done done done done done	am 0 Any four scheduling criteria -1M each eted /hen he in



٦

Sub	ject: Oper	rating System	Subject Code:	22516	
		<ul> <li>Turnaround time: -The time submission of a process to the time is called as turnaround time. It is twaiting to get into the memory, executing with the CPU, and doing I</li> <li>Waiting time: - It is the sum of time queue by a process. When a process loaded into the main memory. A proceed of the comparison of the comparison of the main memory. A proceed of the comparison of the c</li></ul>	of completion of that pro- the sum of time period sp waiting in the ready qu I/O operations. me periods spent in the re is selected from job pool,	cess pent eue, eady it is	
3.	a) Ans.	Attempt any <u>THREE</u> of the following Draw and explain process state diagra Different process states are as follows: 1. New 2. Ready 3. Running 4. Waiting 5. Terminated New admitted interrupt ready //O or event completion waiting New: When a process enters into the sy state a process is created. In new state the Ready: When the process is loaded into for execution. In this state the proce allocation.	exit terminated running tch 1/0 or event wait estem, it is in new state. In the process is in job pool.	ady	M cess tte ram M nation



Subject: Ope	rating System Subject Code:	22516	
	<b>Running:</b> When CPU is available, system selects one process from main memory and executes all the instructions from that process. So when a process is in execution, it is in running state. In single u system, only one process can be in the running state. In multiu system, there can be multiple processes which are in the running state.	So, ser ser	
	<b>Waiting State</b> : When a process is in execution, it may request for l resources. If the resource is not available, process goes into available, waiting state. When the resource is available, the process goes back ready state.	the	
	<b>Terminated State:</b> When the process completes its execution, it goes into the terminat state. In this state the memory occupied by the process is released.	ted	
<b>b</b> )	Describe conditions for deadlock prevention.	4N	Л
Ans.	By ensuring that at least one of below conditions cannot hold, we can prevent the occurrence of a deadlock.	an Any j condia	
	<b>1.Mutual Exclusion:</b> The mutual-exclusion condition must hold for non-sharal resources. Sharable resources do not require mutually exclusion access, thus cannot be involved in a deadlock.		ach
	<ul> <li>2.Hold and Wait:</li> <li>One way to avoid this Hold and Wait is when a process requests resource; it does not hold any other resources.</li> <li>One protocol that can be used requires each process to request a be allocated all its resources before it begins execution.</li> <li>Another protocol that can be used is, to allow a process to request resources only when the process has none. A process may requires ome resources and use them. Before it requests any addition resources, it must release all the resources that are currently allocated it.</li> </ul>	und est est nal	
	<b>3.No Preemption:</b> If a process that is holding some resources requests another resources that cannot be immediately allocated to it, then all resources current being held are preempted. That is these resources are implicit	tly	



Subject: Oper	rating System Subject Code: 2	2516
	released. The preempted resources are added to the list of resources for which the process is waiting. Process will be restarted only when all the resources i.e. its old resources, as well as the new ones that it is requesting will be available. <b>4.Circular Wait</b> Circular-wait condition never holds is to impose a total ordering of all resource types, and to require that each process requests resources in an increasing order of enumeration. Let $R = \{R1, R2,, Rn\}$ be the set of resource types. We assign to each resource type a unique integer number, which allows us to compare two resources and to determine whether one precedes another in our ordering. Formally, define a one-to-one function F: R _ N, where N is the set of natural numbers.	
c) Ans.	<ul> <li>Explain fixed size memory partitioning.</li> <li>Fixed Size Memory Partitioning (Static)</li> <li>Memory is divided into number of fixed size partitions, which is called as fixed or static memory partitioning.</li> <li>Each partition contains exactly one process.</li> <li>The number of programs to be executed depends on number of partitions.</li> <li>When the partition is free, a selected process from the input queue is loaded into the free partition.</li> <li>When the process terminates, the partition becomes available for another process.</li> <li>The operating system keeps a table indicating parts of memory which are available and which are occupied.</li> <li>Initially, all memory is available for user processes and it is considered as one large block of available memory, a hole.</li> <li>When a process arrives, large enough hole of memory is allocated to the processes.</li> </ul>	Explanation 4M
d) Ans.	<ul> <li>Explain linked file allocation method. Linked Allocation:</li> <li>This allocation is on the basis of an individual block. Each block contains a pointer to the next block in the chain.</li> <li>The disk block can be scattered anywhere on the disk.</li> </ul>	4M Correct explanation 4M,



#### WINTER – 2022 EXAMINATION **MODEL ANSWER**

Sub	ject: Oper	ating System	Subject Code:	22516
Sub	ject: Oper	<ul> <li>The directory contains a pointer to the file.</li> <li>To create a new file, simply create a</li> <li>The following figure shows the link</li> </ul>	the first and the last block	ts of Diagram Optional
		<ul> <li>20 21 22 23 24 25 26 27 28 29 30 31</li> <li>There is no external fragmentation at a time.</li> <li>The size of a file need not be declar</li> <li>A file can continue to grow as long</li> <li>This method is used only for a sequ</li> <li>This method requires more space to</li> <li>So instead of blocks, clusters are creates internal fragmentation.</li> </ul>	ed when it is created. as free blocks are available ential access files store pointers	e
4.	a)	Attempt any <u>THREE</u> of the following Compare between command line ar (Any four points)		face. 12 4M



#### MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION (Autonomous)

(ISO/IEC - 27001 - 2005 Certified)

#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System

Subject Code:

Ans.	Parameter	Command Line Interface(CLI)	Graphic User Interface(GUI)	Any four points 1M each
	Definition	Interaction is by typing commands	Interaction with devices is by graphics and visual components and icons	
	Understan ding	Commands need to be memorized	Visual indicators and icons are easy to understand	
	Memory	Less memory is required for storage	More memory is required as visual components are involved.	
	Working Speed	Use of keyboard for commands makes CLI quicker.	Use of mouse for interaction makes it slow	
	Resources used	Only keyboard	Mouse and keyboard both can be used	
	Accuracy	High	Comparatively low	
	Flexibility	Command line interface does not change, remains same over time	Structure and design can change with updates	
b) Ans.	System calls of 1. create 2. delete 3. open f 4. close f 5. create 6. delete 7. read, w 8. getfile	existing file ile		4M Any 4 system calls 1M each



Subj	ject: Oper	rating S	System	Subject Code:	22516
	c)		oare between Long term and points)	d short term scheduler. (A	_
	Ans.	Sr. No	Long Term Scheduler	Short Term Scheduler	Any four points 1M each
		1	It is job scheduler	It is CPU scheduler	
		2	It selects processes from job pool and loads them into memory for execution	It selects processes from ready queue which are ready to execute and allocates CPU to one of them	
		3	Access job pool and ready queue	Access ready queue and CPU	
		4	It executes much less frequently. It executes when memory has space to accommodate new process.	It executes frequently. It executes when CPU is available for allocation	
		5	Speed is less than short term scheduler	Speed is fast	
		6	It controls the degree of multiprogramming	It provides lesser control over degree of multiprogramming	
		7	It chooses a good process that is a mix-up of input/output bound and CPU bound.	It chooses a new process for a processor quite frequently.	
	d)	algori	given problem by using thm using Gantt chart. Calc ch algorithm		0



(ISO/IEC - 27001 - 2005 Certified)

Subject: Oper	rating System	L	Subject Co	de:	22	2516						
	Proce	ess	Burst time (in ms	5)								
	P1		9									
	P2		7									
	P3		3									
	P4		7									
Ans.	Gantt Char											
	P3	P2	P4		P1		For each					
	0 3		10	17		26	scheduling Gantt chart					
	Waiting Tin	ne					1М,					
	P1=17 P2=3						Each					
	P3=0						average					
	P4=10						waiting time					
		iting tin	ne=Waiting time of	f all processe	s / Numb	er of	calculation 1M					
	processes	U	C	-			1171					
			=(17+3+0+10)	) /4								
			=30/4									
			=7.5 millis	seconds (ms)								
	Gantt Char											
	P	1	P2	P3	P4							
	0		9 16		19 26							
	Waiting Tin	ne										
	P1=0											
	P2=9											
	P3=16											
	P4=19	iting tin	-Waiting time	f all propage	a / Numb	on of						
	processes	iung un	ne=Waiting time o	i all processe	s / Inumb	er of						
	processes		=(0+9+16+19)	) /4								
			=44/4	, · -								
	=	=11 milli	seconds (ms)									



Г

#### (ISO/IEC - 27001 - 2005 Certified)

Subject: Ope	rating	g Sys	stem								Sub	ject	Cod	e:		22	2516	
e) Ans.	there disk bloc 1)B The Each the b For 13 a	le s efore The ks ir 1. 2. <b>it V</b> free h block exam re fr	ysten e it 1 re a n the Bit V Link ector -space- space is a nple, ee an	m is has re f disl Vect ced l r: ce li s rep llloc , con nd th	to k to k main c are tor List st is prese ated nside ne re	pons teep lly f man imp entec , the er a c st of	sible trac four nage lema l by bit disk	to k of app d. entec 1 bi is 0. whe bloc	alloo all as roac t. If re b ks a	cate the hes a bit the lock re al	the free by map bloc	free blo using or b k is f 3, 4, ted.	bloc cks j g wł it ve free, 5, 8,	ks to prese tich, ctor. the b	nt in the	file the free 1; if , 12,	An techn	iques rect nation
	$\begin{bmatrix} 0 \\ 0 \\ 1=Fi \\ 0=A \\ The effic \\ on the effic \\ 0 \\ In the block \\ the value \\ also \\ toge \\ tog$	1 oree the function of the fun	2 1 block cated n ad y in sk. ed La ppro- ntain first hed , kee	3 1 blo vant find sach ns a disl in 1 ping	4 1 ck tage ding , the poin k blo mem g a p	5 1 of the the e free nter ock i oory.	6 0 first e dis to th s sto In er to	7 0 free sk bl ne ne pred this o the	8 1 block e blo lock ext f at a app first	9 1 is it ck c s are ree t sepa roac	10 1 ts rel or n o e lind olock arate ch, li	11 1 ative conse ked to t. Th loca nk a ck. T	12 1 e sim ecutiv toget e blo tion ll th	her i bek n on di e dis	.e. a umbo isk an sk bl	15 0 d its ocks free er of nd is ocks tains		



(ISO/IEC - 27001 - 2005 Certified)

		2 EXAMINATION L ANSWER						
Subject: Oper	rating System	Subject Code:						
	fres-space list head 0 4 4 12 16 20 24 28	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$						
5. a)	Attempt any <u>TWO</u> of the foll Write two uses of following C (i) Device Management	-	12 6M					
	<ul><li>(ii) Performance monitor</li><li>(iii) Task Scheduler</li></ul>		2 uses of each tool 2M					
Ans.	<ul> <li>Allow interaction with hardy</li> <li>Used to install device and associated software.</li> <li>Allocate devices to the propriority.</li> <li>Deallocate devices either the on condition.</li> <li>Keeping track of all device?</li> <li>Monitoring device status lidevices.</li> <li>Used to enforce the preder process receives the device view of the device of the</li></ul>	ke printers, storage drivers an termined policies and decides	ver. well as ent and pending nd other s which memory					



MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION

(Autonomous)

(ISO/IEC - 27001 - 2005 Certified)

#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System

Subject Code:

		iii) Ta	sk :	sch	edı	ıleı	•																
		1. Ass	ign	pro	oce	sso	r to	tas	k r	ead	y fo	or e	exec	cuti	on								
		2. Executing predefined actions automatically whenever a certain set																					
		of c	cond	diti	on i	is n	net.																
		(Any t																					
	b)	Write			-	•	s of	f fol	llov	vin	g co	om	ma	nds	5								6M
		(i) Wa			501	8																	
		(ii) Sle	-																				
		(iii) PS								1 41			•			<b>c</b>		c:				Б	
	Ans.	<b>ns.</b> i) Wait command waits until the termination of specified process ID 2385018										2M for each											
		ii) Slee	ep c	com	nma	ınd	is ı	isec	d to	de	lay	for	· 9 s	seco	ond	s d	urir	ng t	he	exe	cut	ion	correct
Í Í		of a	-								-							-					output
		iii) ps	co	mn	nan	d v	vitł	1 -U	ı is	s us	sed	to	dis	spla	y o	data	ı/pr	oce	esse	s f	or	the	
		specifi																					
	c)	Given																					6M
		Calcu				-	<u> </u>						-	ptin	nal	' '	and	•	LR	U'	pa	age	
		replac																					
	Ang	<b>'7,0,1</b> ,						~ ~				·			dor								
	Ans.	(Repro			uo	n o		am	e ca	an	bel	n a	iny	ore	uer	)							Calculate
		I) Opt	11116	41																			page fault with
		Ref	7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1	relevant
		F1	7	7	7	2	2	2	2	2	2	2	2	2	2	2	2	2	2	7	7	7	diagram- 3M each
		F2		0	0	0	0	0	0	4	4	4	0	0	0	0	0	0	0	0	0	0	
		F3			1	1	1	3	3	3	3	3	3	3	3	1	1	1	1	1	1	1	
		Fault	F	F	F	F		F		F			F			F				F			
		Total p	bag	e fa	ult	s- 9	)	1	I	1	I	I	I	<u> </u>	I	I	I			<u> </u>	I		
		1	. 0																				
		ii) LR	U																				
		Ref	7	0	1	2	0	3	0	4	2	3	0	3	2	1	2	0	1	7	0	1	
		F1	7	7	7	2	2	2	2	4	4	4	0	0	0	1	1	1	1	1	1	1	
		F2		0	0	0	0	0	0	0	0	3	3	3	3	3	3	0	0	0	0	0	
		F3			1	1	1	3	3		2	2	2	2	2	2	2	2	2	7	7	7	
		Fault	F	F	F	F		F		F	F	F	F			F		F		F			
1									-										-				1
		Total p	pag	e fa	ult	s-12	2																



MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION

(Autonomous)

(ISO/IEC - 27001 - 2005 Certified)

Subj	ject: Ope	rating System			Subject Code:	22516
6.	a)	Attempt any <u>T</u> Solve given pro (i) Pre-emptive (ii) Round Rob Calculate avera	blem by usin SJF in (Time Slic	e = 3 ms	antt Chart	12 6M
		Process	A.T.	B.T. (in ms)		
		P <sub>11</sub>	0	8		
		P <sub>12</sub>	1	4		
		P <sub>13</sub>	2	9		
		P <sub>14</sub>	3	5		
	Ans.	(*) <b>D</b>	CIE.			
	A115.	(i) Pre emptive	SJF:	12 P14	P11 P13	Each method 3M
		0 1	2 3	5	10 17 20	- 1M for Gantt chart,
		Waiting Time= time	: (Total com	pletion time	e –Burst time ) – Arr	waiting time
		P11 –(17-8)-0 =	9ms,			calculation,
		P12- (5 – 4) -1				1M for
		P13- (26-9)-2 =	,			Average waiting time
		P14- (10-5)-3 =		+15+2)/4=24		
		Average waiting	; time :- (9+0	+15+2)/4= 20	0/4=0.3 IIIS	
		(ii) Round Rob	in (Time Slic	e = 3 ms)		
		P11 P12 0 3 6	P13 P14 9 12		P13 P14 P11 P13 19 21 23 21	6
		Waiting time: - I P12-(16 – 4)- 1 = P13-(26-9)-2 =1 P14-(21-5)-3 =1 Average waiting	=11ms, 5ms, 3ms		=54/4= 13.5ms	



(ISO/IEC - 27001 - 2005 Certified)

Subj	ect: Oper	rating System		Subject Code:	22	2516	
	b)	Consider the followin P4 comes with memor Locate (Draw) this pr	ry requirements o		cess	6N Each	ı fit
			O.S. P1 <free> 12 KB P2 <free> 19 KB P3 <free> 7KB Memory</free></free></free>			diagrai	n 2M
	Ans.	First Fit: Allocate the first free block to the new process P4. O. S. P1 P4 6KB <free> 6KB P2 <free> 19 KB P3 <free> 7 KB</free></free></free>	the smallest the block that is enough	big block to the ne process P4. ew O. S. P1 S CFREE> 12 KB P2 P2 P4 6 KB CFREE> 13 KB P3	ree ew		
	c)	Construct and explain terms of two level and	e	ıre of a file system in		6N	1
	Ans.	(UFD). The UFD lists master file directory account number. Each When a user refers to a	ures, each user ha only files of a sin (MFD) which is entry in MFD poin a particular file, on ve files with the sa	s its own user file direct igle user. System containdexed by user name ts to the UFD for that use ly his own UFD is search ime name, as long as al	ins a e or ser. shed.	Explan of strue 2M ec Constr n o struct 1M ec	cture ach, uctio of ture



#### WINTER – 2022 EXAMINATION MODEL ANSWER

#### Subject: Operating System



